

ECAD & VLSI lab course outcomes

At the end of the course a student will be able to:

CO1: Design Entry & simulation of Multiplexer circuit with test bench & functional verification.

CO2: Design Entry & simulation of D flip-flop circuit with test bench & functional

CO3: Synthesis, P&R and Post P&R simulation for Full adder, Concepts of FPGA floor plan, critical path, design gate count, I/O configuration and pin assignments.

CO4: Generation of configuration/fuse files for 4:1 multiplexer & D flip-flop & implementation of the hardware using FPGA.

CO5: Design a schematic and simple layout for CMOS Inverter, parasitic extract.