



Hyderabad Institute of Technology And Management



ISSUE of Jan 2025– June 2025

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Where dreams find
their path to reality.



Department of ECE News Letter .

Editorial Team: Dr. S V Devika

(Editor-in-chief) .

Mr. Kondalarao Punati

(Editor)

Chief Patrons :

▪ **Sri. Prashanth Arutla**
(Chairman Royal Educational Society)

▪ **Dr. S Arvind**
Principal, HITAM



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NEWS LETTER

Vision and Mission of Institute

Vision of Institute:-

“To be a role model technological university of national repute that imparts research-based multi-disciplinary competencies in students to enable their career aspirations and contribute to society.”

Mission of Institute:-

Build students' competencies through HITAM's 'Doing Engineering' approach with relevant curriculum, pedagogy and assessment.

Collaborate with industry and institutions for capacity building in research, innovation and real time knowledge.

Develop employability skills for emerging trends and societal needs

Excel by adopting NEP 2020 and improving accreditations & national rankings.



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NEWS LETTER

Vision and Mission of Department

Vision of Department:-

“To achieve academic and research excellence in Electronics and Communication Engineering by imparting multidisciplinary skills that prepare students to meet industry needs, achieve career goals, and contribute to society.”

Mission of Department:-

To build a strong foundation in Electronics and Communication Engineering with a focus on practical skills, problem-solving and technical knowledge.

To promote a student-centric ecosystem that promotes innovation, multi-disciplinary learning, and collaborative research in emerging technologies.

To nurture socially responsible engineers by encouraging sustainable practices, ethical values, and active engagement with industry and community.



Program Educational Objectives (PEOs)

Program Educational Objectives

PEO1: *To exhibit strong team collaboration and professional competence in both IT and core electronic sectors.*

PEO2: *To excel in higher education and research with strong technical knowledge, critical thinking, and innovative skills in emerging technologies.*

PEO3: *To address society needs through multidisciplinary engineering projects approach.*



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NEWS LETTER

Achievements in
January

- 21E51A0440 PALAKONDU RICHITHA got Paid (12k per month) internship in Tractrix.
- 22E51A0436-PEDDINTI HERAMBA SAI GANESH, 22E51A0449-SHAIK THOUHID ALI and 22E51A0419-GOTETI KRITIKA selected in SSG core Team for next Academic year.
- 22E51A0436-PEDDINTI HERAMBA SAI GANESH shortlisted for the Samsung Fellowship as part of Cohort 4 of the India Semiconductor Workforce Development Program (ISWDP) in in the Grade III category.



NEWS LETTER

FDP & Publications

- Conducted online FDP on “**New Frontiers AI/ML to Social Need Applications - Medical Devices** ” collaboration with E&ICT NIT Warangal.
- Dr. S. V. Devika and Dr. S Arvind published a paper (Scopus) “From Lecture-Based Learning to Problem Based Learning: A Review on Navigating the Transformation in Engineering Education ” in Journal of Engineering Education Transformations.
- Dr. S. V. Devika, Dr. S Arvind and Kondala Rao published a paper (Scopus) “Enhancing Program Outcomes and Academic Performance through Problem-Based Learning in an Antennas and Wave Propagation Course” in Journal of Engineering Education Transformations.
- P Kondala Rao presented a paper in IEEE conference “Empowering DR assessment: A DenseNet-CBAM Driven Approach.
- V M Rani Published a paper in IEEE conference ”Multi-Phase Multi Level DC-DC Converter for Renewable Energy Systems”.

NEWS LETTER

VLSI-COE Inauguration



**EXPLORE
MINIATURE MAGIC AND
EXPERIMENT
INCREASED INTEGRATION**

Launch of
Center of Excellence
for 'VLSI' at HITAM

24th JAN '25

VLSI

Center of Excellence

HITAM

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Madhwal, Hyderabad, India



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NEWS LETTER

- Webinar on “Unlocking VLSI” organized along with IEEE student chapter on 7.2.2025

Webinar in February

**FUTURE OF VLSI:
Opportunities unlimited
(in a world driven by
AI & Automation)**

UNLOCKING VLSI
Careers, Opportunities &
Future Trends

by 
MR. SARMA YANAMANDRA
CEO, SiliconPatterns

Organized by
VLSI Center of Excellence at HITAM
in collaboration with
IEEE HITAM Student Branch

7th FEB 2025 | 2:00 PM - 3:30 PM
@ GUS (CofE VLSI)

In Association with  

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Medchal, Hyderabad, India.



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Publications in March

- Dr. J Rajeshwar Goud published a paper (Scopus) “Innovative accident prediction: A regularized extreme learning machine model approach ” International Conference on Hybrid and Advanced Technologies (ICHAT 2024),Taylor & Francis Feb 2025.
- P Santhosh published a paper (Scopus) “A Novel ASK Reversible Gate and Its Implementation in Ripple Carry Adder Design” 2024 IEEE International Conference of Electron Devices Society Kolkata Chapter (EDKCON), Kolkata, India, 2024

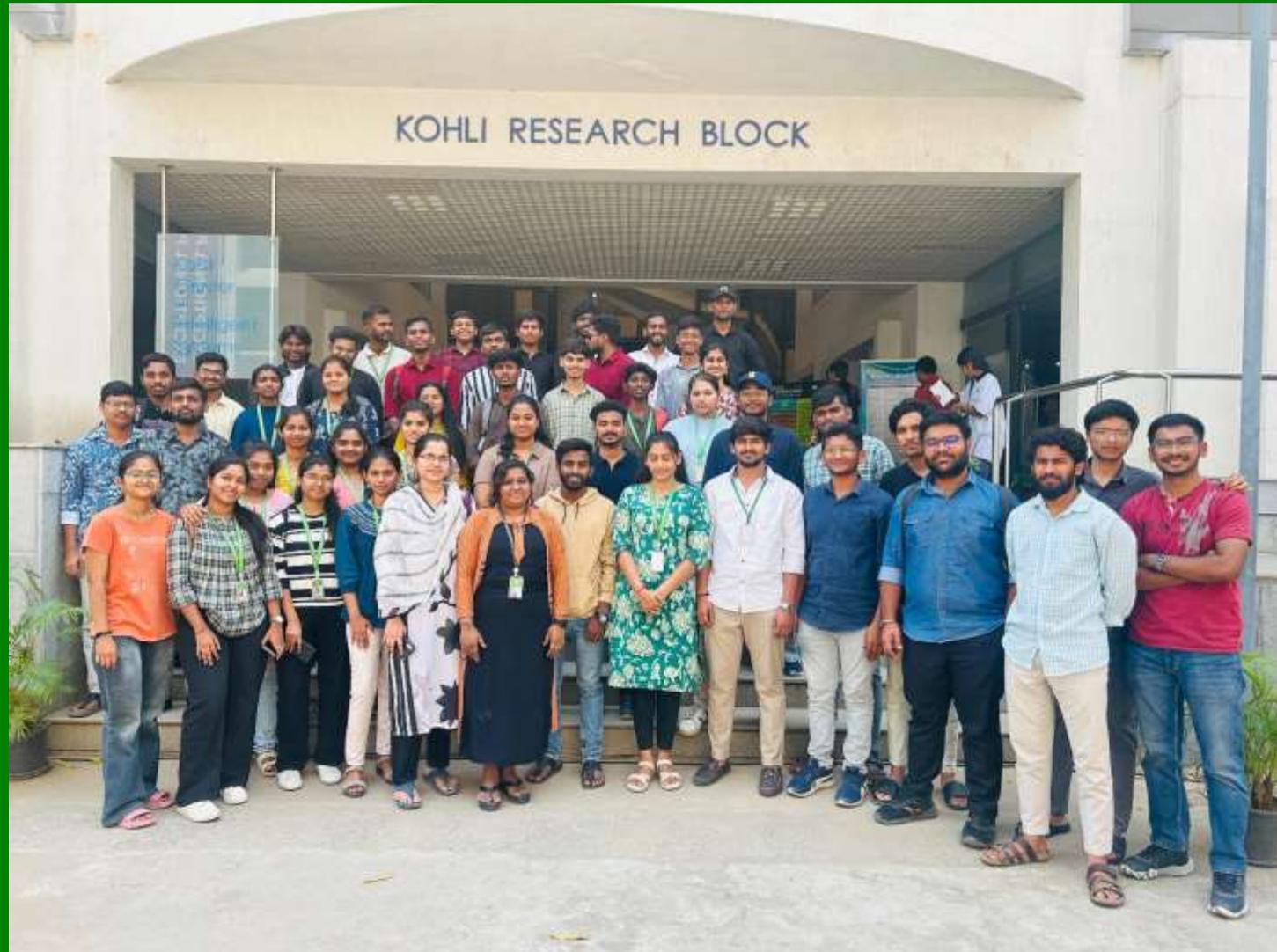


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NEWS LETTER

Visit to IIIT Hyderabad





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NEWS LETTER

Visit To T Works





NEWS LETTER

Student Achievements:

- 22E51A0419 GOTETI KRITIKA, 22E51A0449 SHAIK THOUHID ALI, 22E51A0436 PEDDINTI HERAMBA SAI GANESH and 22E51A0424 INAMDAR SUDESH are selected for 3 months internship in IIIT Hyderabad.
- Conducted 2-Day workshop on PCB Design in collaboration with IETE Student chapter.
- 21E51A0457 UTKARSH KUMAR awarded best cadet.

Achievements in April

OUT TO PROVE
A THING OR TWO!



KRITIKA
3rd year, ECE



THOUHID ALI
3rd year, ECE



HERAMBA SAI GANESH
3rd year, ECE



SUDESH INAMDAR
3rd year, ECE



RACHIT REDDY
3rd year, CSO

Congratulations to these HITAMights for getting selected as INTERNS for a 3-month College Research Affiliate Program at IIIT, Hyderabad, with IoT as their area of Interest.

Congratulations!



NEWS LETTER

- Dr. K Bindu Madhavi, Dr. J Rajeshwar Goud, Dr.SV Devika, Dr. B Lokeswara Rao, Kondalarao Punati, Tejaswi Vallabhapurapu, P.N.V. Naveen Kumar, T. Venkanna Babu, **VLSI Implementation of Pulse Width Modulation Unit for High-Fidelity Audio Signal Processing**, Indian Patent.

Publications in April

- Dr. Devika SV published a IEEE conference paper **“Optimizing Urban Solid Waste-to-Energy Production Systems with Backpropagation Neural Network Model”** in International Conference on Integrated Circuits and Communication Systems (ICICACS) April 2025.



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NEWS LETTER

- Signed an MoU with Robomonk Technologies to promote collaboration and innovation. Also conducted Two day workshop on PCB design

Workshop on PCB Design

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POWER YOUR WAY UP!

2-Day workshop on PCB DESIGN

by Robomonk Technologies
In collaboration with
IETE HITAM Student Branch

25-26 APR 2025
9:00 AM - 4:00 PM
@G04, HITAM

IEEE HSB ComSoc
HITAM

IETE
The Institution of Electronics & Telecommunication Engineers

CHIEF PATRON Dr. ARVIND Principal HITAM	CONVENORS MR. K. SURESH HOD-EEE HITAM	DR. J. RAJESHWAR GOUD HOD-ECE HITAM	CO-CONVENORS DR. K. BINDU MADHAVI Assoc. Professor, ECE HITAM	DR. M. CHIRANJIVI Assoc. Professor, EEE HITAM	CO-ORDINATORS MR. P. SANTOSH Asst. Professor, ECE HITAM	MR. M. SIDDHARTH Asst. Professor, EEE HITAM
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Medchal, Hyderabad, India.



NEWS LETTER

Student Achievements

- 23E55A0403-JANGA AKSHAYA successfully completed NPTEL course “Digital System Design” for Jan-Apr 2025.
- 22E51A0453-SUNKI AKHILA successfully completed NPTEL course “Digital System Design” for Jan-Apr 2025.
- 22E51A0407-CHINNAM MANISHA successfully completed NPTEL course “Digital System Design” for Jan-Apr 2025.
- 22E51A0454-SUREDDI ALEKHYA successfully completed NPTEL course “Digital System Design” for Jan-Apr 2025.
- 22E51A0446-SANGIDI ANUSREE successfully completed NPTEL course “Cloud Computing” for Jan-Apr 2025.
- 22E51A0434-PACHIGOLLA AKSHITHA successfully completed NPTEL course “Embedded Systems Design” for Jan-Apr 2025.
- 22E51A0425-KANDREGULA ANUSHA successfully completed NPTEL course “Embedded Systems Design” for Jan-Apr 2025.
- 22E51A0436 PEDDINTI HERAMBA SAI GANESH selected as Green Intern under 1M1B Green Internship Program.



NEWS LETTER

Student Achievements

- Mr P Santhosh successfully completed NPTEL course “Cryptography and network security” for Jan-Apr 2025.
- Mr P Santhosh successfully completed NPTEL course “Computer networks and internet protocol” for Jan-Apr 2025.
- Dr. J Rajeshwar Goud successfully completed NPTEL course “RF Transceiver Design” for Jan-Apr 2025.
- Mrs. V M Rani successfully completed NPTEL course “Cloud computing” for Jan-Apr 2025
- Mr. T Venkanna babu successfully completed NPTEL course “Microprocessor and Microcontroller” for Jan-Apr 2025.



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NEWS LETTER

Students of ECE attended Project EXPO and presented their Major Projects.

Project Expo

