V.MOSHE RANI

	**Name :Velicharla Moshe Rani** **Designation : Assistant.Professor** **Total Experience :7** **Email:ranim.ece@hitam.org**
Qualification	M.Tech / M.E. Details:VLSI system Design
Total Teaching Experience	4
Publication Details	**International Journals:**
	1.Implementation of efficient convolution encoder and modified viterbi decoder
	2.Performance Analysis of a 64-bit signed Multiplier with a Carry Select Adder Using VHDL
	3.Minimizing the Sub Threshold Leakage for High Performance CMOS Circuits Using Stacked Sleep Technique
Awards & Honours	…. . ..NIL….
Workshops / Seminars / Conferences / Training Programs Attended	1.ATTEDENDED IEEE EVNTS
	2.PARTICIPATED IN MICROCONTROLLER WITH ARM7
	3. PARTICIPATED IN WOMEN DAY CELEBRATIONS UNDER IEEE
	4. PARTICIPATED IN RASPARI PAI WORKSHOP
	5.PARTICIPATED IN PCB DESIGN
	6.PARTICIPATED IN DST WORKSHOP

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Workshops / Seminars / Conferences / Training Programs Conducted	…..NIL….