

ANIL KUMAR . K



Name :ANIL KUMAR .K

Designation : Assistant Professor

Total Experience : 9 years

Email:anilkumark.ece@hitam.org, anilkumar10436@gmail.com

Mobile: 9866072284

Specialization

Embedded Systems

Academic Qualification

July 2004-July 2008: Bachelors of Technology in Electronics and Communication Engineering, from Jawaharlal Nehru Technological University Hyderabad, TS, India.

August 2008- July2011: Masters of Technology. in in Electronics and Communication Engineering, from Jawaharlal Nehru Technical University, Hyderabad,TS India.

Total Teaching Experience

7 years

Publication Details

International Journals:

High Throughput VLSI Architecture for Montgomery Modular Multiplication using Carry Save Adder,International Journal of Professional Engineering Studies Volume 9 /Issue 4 / Dec 2017

Delay Efficient Hybrid- Double Multiplication Based on Mastrovito Multipliers ,International journal of Eminent Engineering Technologies Volume 5, Issue I Nov 2017

An Efficient Permanent Fault Detection Method in FIFO Buffers of NoC Routers,International Journal of Emerging Technologies in Engineering Research (IJETER) ISSN: 2454-6410 Volume 5, Issue 11, November (2017)

Design of Low Power Adder in ALU Using Flexible Charge Recycling Dynamic Circuit, International Journal of Research ISSN: 2348-795X Volume 04 Issue 14 November 2017

Design of Supergate based on Grap-based Transistor Network,International Journal of Advanced Research Trends in Engineering and Technology (IJARTET) ISSN 2394-3785 Vol. 4, Issue 2, January 2017

Aging-Aware Reliable Multiplier Design with Adaptive Hold Logic Implements With 4x4 Vedic Multipliers,International Journal of Innovative Technologies ISSN 2321-8665 Vol.04,Issue.15,October 2016,

Implementation Enhanced Majority Logic Decoding of Low Density Parity Check Codes for Error Detection,International Journal of Eminent Engineering Technologies Volume4, Issue 5 Oct 2016

Adaptive biased operational amplifier with improved slew rate ,International Journal of Embedded & VLSI System ISSN: 2349 8129
Volume 3, Issue 10 Oct 2014

International Conferences:

ANIL KUMAR . K

Group Discussion-Debate Approach to Teach DDTV (Digital Design through Verilog) ICTIEE at Thiagarajar College of Engineering during 8th January 2017-9th January 2018 at Madurai

Awards & Honours

Got Appreciation for Effective Teaching & Learning Methods from MISSION 10X Aarambh

Attended 2 days International Conference on Transformation in Engineering Education at Thiagarajar College of Engineering during 8th January 2017-9th January 2018 at Madurai

Attended 3 days International Conference on Transformation in Engineering Education at Vardhaman College of Engineering during 6th January 2017-8th January 2017 at Hyderabad

OBE Certificate from IUCEE for OBE course conducted by KLE Tech University,Hubli, Karnataka

Completed IUCEE –IIEECP pre certification

Workshops / Seminars / Conferences / Training Programs Attended

Attended 2 days workshop on PCB Design at HITAM,Hyderabad

Attended 5 days workshop on Faculty Immersion Program at K.L.E.Technological University Hubli, Karnataka

Attended 5 days workshop on VLSI Design using Cadence Tools at CMRIT,Hyderabad

Attended 3 days workshop on Intel@Embedded Initiative Educator Development Program at JNTUH Hyderabad

Attended 7 days workshop on Faculty Development Program at BVRIT,Hyderabad

Attended 2 days workshop on ARM 7 at HITAM, Hyderabad

Attended 2 days National Level Workshop On Electronic Design Automation (EDA) Tools for Communications at HITAM at Hyderabad

Workshops / Seminars / Conferences / Training Programs Conducted

Conducted 2 days workshop on IOT with Raspberry Pi in February 2018