

E.DEEPTHI



Name :Mrs.E.Deepthi

Designation : Assistant professor

Total Experience :3 years

Email:deepthie.ece@hitam.org

Specialization

VLSISD

Academic Qualification

2009-2012:B.Tech Electronics and communication engineering

2013-2014:M.Tech vlsisd

Total Teaching Experience

3 YEARS

International Journals:

A configurable bus tracer for error reproduction in post silicon validation. **IJECS** issue 1st jan2015-Pg.no (9994-9998).

Performance analysis of a 64-bit signed multiplier with a carry select adder using VHDL. In**IJIRCST** issue 6th Nov2014-ISSN-

visual based activity paper related to EPICS, ICTIEE.

Publication Details

International Conferences:

National Conferences:

Awards & Honours

International level papers published

Organized workshop on ARM processor.

E.DEEPTHI

**Workshops / Seminars /
Conferences / Training
Programs Attended**

District level player in games.

Attended FDP on DSP at JNTUH