

Dr Rahul Vivek Purohit



Name : Dr Rahul Vivek Purohit

Designation : Professor & Head of the Department

Total Experience :13 Years

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Specialization

Microwave Engineering, Pattern recognition techniques

Academic Qualification

B.Tech / B.E. Details: 2002, Electronics Engg., RGPV Bhopal

M.Tech / M.E. Details: 2005, Microwave Engg., RGPV Bhopal

PhD Details: 2014, Jamia Millai Islamia University, New Delhi

Total Teaching Experience

13 Years

International Journals:

Rahul Vivek Purohit, Syed .A. Imam “Speaker-independent emotion recognition: Features and Classifications” International journal of computer science issues, vol 10, issue 2, (April 2013)

Rahul Vivek Purohit, S. A. Imam “Learning role of facial marks in automatic face recognition for forensic science applications” in International journal of engineering and advanced technology, ISSN 2249-8958. Vol. 2, No. 4, (march-2013) Page: 436-447.

Rahul Vivek Purohit, S. A. Imam “Combining speech and gender classification for effective emotion recognition” in International Journal of computer applications E-ISSN 0975-8887 Vol. 64, issue-4, (February 2013) Page: 15-18.

Rahul vivek purohit, S. A. Imam “Recognizing gender with fingerprints” in International Journal of Advanced Engineering & Technology E-ISSN 0976-3945 Vol. 2, issue-4, October-Dec 2011 Page: 239-241.

Rahul vivek purohit, S. A. Imam “Finger print matching in India: A Review” in Journal of Engineering Research and Studies E-ISSN 0976-7916, Vol. 1, issue-2, (October-Dec 2010) Page: 51-59.

Rahul vivek purohit, S. A. Imam “An evaluation of face recognition techniques :A critical review” in MASAUM Journal of Basic and Applied Sciences, ISSN 2076-5932. Vol. 1, No. 3, (October 2009) Page: 436-447.

Rahul vivek purohit, S. A. Imam “A practical approach for retinal authentication” in AKGEC Journal of electronics, ISSN 2076-5932. Vol. 1, No. 1, (October 2009) Page: 436-447.

Rahul vivek purohit, S. A. Imam “Feature Extraction Pattern Recognition: A Review” in MASAUM Journal Of Reviews and Surveys, ISSN 2076-5932. VOL- 1, Issue-1. Volume 1 Issue 1, (September 2009) Page: 27-45.

Publication Details

Dr Rahul Vivek Purohit

	<p>Saumya Jain, Dr. R.V.Purohit, 2015, Microstrip feed dual band notched patch antenna with E- Slots for Ultra Wideband, IJSERT, Vol. 4, No. 47, PP. 10280-10284.</p> <p>Saumya Jain, Dr. R.V.Purohit, 2015, A dual band notched patch antenna with Circular Slots for UWB, International journal of Advanced Research in Electronics & Communication Engineering (IJARECE),, Vol. 4, No.12, PP. 2902-2909.</p>
Awards & Honours	Co-ordinator, “MOOCs” through ‘SWAYAM’, “UNNAT BHARAT ABHIYAN” and VITTIYA SAKSHARTA ABHIYAN “VISAKA”, AKGEC, Ghaziabad
	Management Representative of ISO 9001:2008 for the college since 2011 to 2017, AKGEC, Ghaziabad
	M.Tech (ECE & VLSI) Co-coordinator, AKGEC, Ghaziabad
	In-charge of research & development section of ECE Deptt. , AKGEC, Ghaziabad
	In charge of the Committee formed by AKGEC, Ghaziabad for National Assessment and Accreditation Council (NAAC)
	In charge of the Committee formed by AKGEC, Ghaziabad for National Board of Accreditation (NBA) certification.
Workshops / Seminars / Conferences / Training Programs Attended	Attended five days FDP on “Satellite Communication” organized by Dr. A.P.J. Abdul Kalam Technical University, Lucknow and AKGEC, Ghaziabad from 14–18 June, 2016.
	Attended five days FDP on “Digital signal processing” organized by Dr. A.P.J. Abdul Kalam Technical University, Lucknow and ABES-EC, Ghaziabad from 07–11 June, 2016.
	Attended five days FDP on “Wireless & Mobile Communication” organized by Dr. A.P.J. Abdul Kalam Technical University, Lucknow and AKGEC, Ghaziabad from 4–8 January, 2016.
	Attended five days FDP on “Global System for Mobile Communication” organized by NITTTR Chandigarh at AKGEC, Ghaziabad from 7–11 September, 2015.
	Attended National Conference on “Broadband and Optical Fibre Technologies” organized by ABES Engineering College, Ghaziabad on 13–14 March, 2015.
	Attended IEEE MTT-S Workshop on “RF and Microwaves for the Cellular and Industrial Applications” on 13 December, 2014 at Ambedkar Institute of Advanced Communication Technologies & Research, Delhi.
	Organized two day workshop on “Lean Six Sigma Yellow Belt” at AKGEC, Ghaziabad from 29–30 August, 2014.
Workshops / Seminars / Conferences / Training Programs Conducted	Conducted Two day workshop on PCB Design in February 2018